

35760 U.S. PTO  
10/015434

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U.S. UTILITY Patent Application

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PATENT NUMBER and  
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10015434	12/13/2001	438	342	2823	K. NGUYEN

\*\*APPLICANTS: Howard Bradley;

\*\*CONTINUING DATA VERIFIED:

This application is a DIV of 09/146,263 09/03/1998 PAT 6,350,706

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\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		97-0008.01
Verified and Acknowledged Examiners's initials		
TITLE : Process for using photo-definable layers in the manufacture of semiconductor devices and resulting structures of same		

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
Amount Due	Date Paid	Sheet Drwg.	Figs. Drwg.
		Print Flo.	
Primary Examiner		Applicati n Examiner	
PREPARED FOR ISSUE			
<input type="checkbox"/> TERMINAL DISCLAIMER		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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